XGSF-T12-02-04 SFP Transceiver

COMCASTGROUP

Content

Description

XGIGA'S XGSF-T12-02-4 10/100/1000 BASE-T Copper Small Form Pluggable (SFP) transceivers are based on the SFP Multi Source Agreement (MSA) . They are compatible with the Gigabit Ethernet standards as specified in IEEE Std 802.3. The 10/100/1000 BASE-T physical layer IC (PHY) can be accessed via I2C, allowing access to all PHY settings and features.

The XGSF-T12-02-4 is compatible with 1000BASE-X auto-negotiation and have a link indication feature. PHY is disabled when TX disable is high or open.

Features

- Up to 1.25 Gb/s bi-directional data links
- Hot-pluggable SFP footprint
- Low power dissipation (1.05W typical)
- Compact RJ-45 connector assembly
- Fully metal enclosure, for lower EMI
- RoHS compliant and lead-free
- Single +3.3V power supply
- 10/100/1000 BASE-T operation in host systems with SGMII interface
- 1.25 Gigabit Ethernet over Cat 5 cable
- Case operating temperature：
  Commercial: 0°C to +70°C
  Extended: -10°C to +80°C
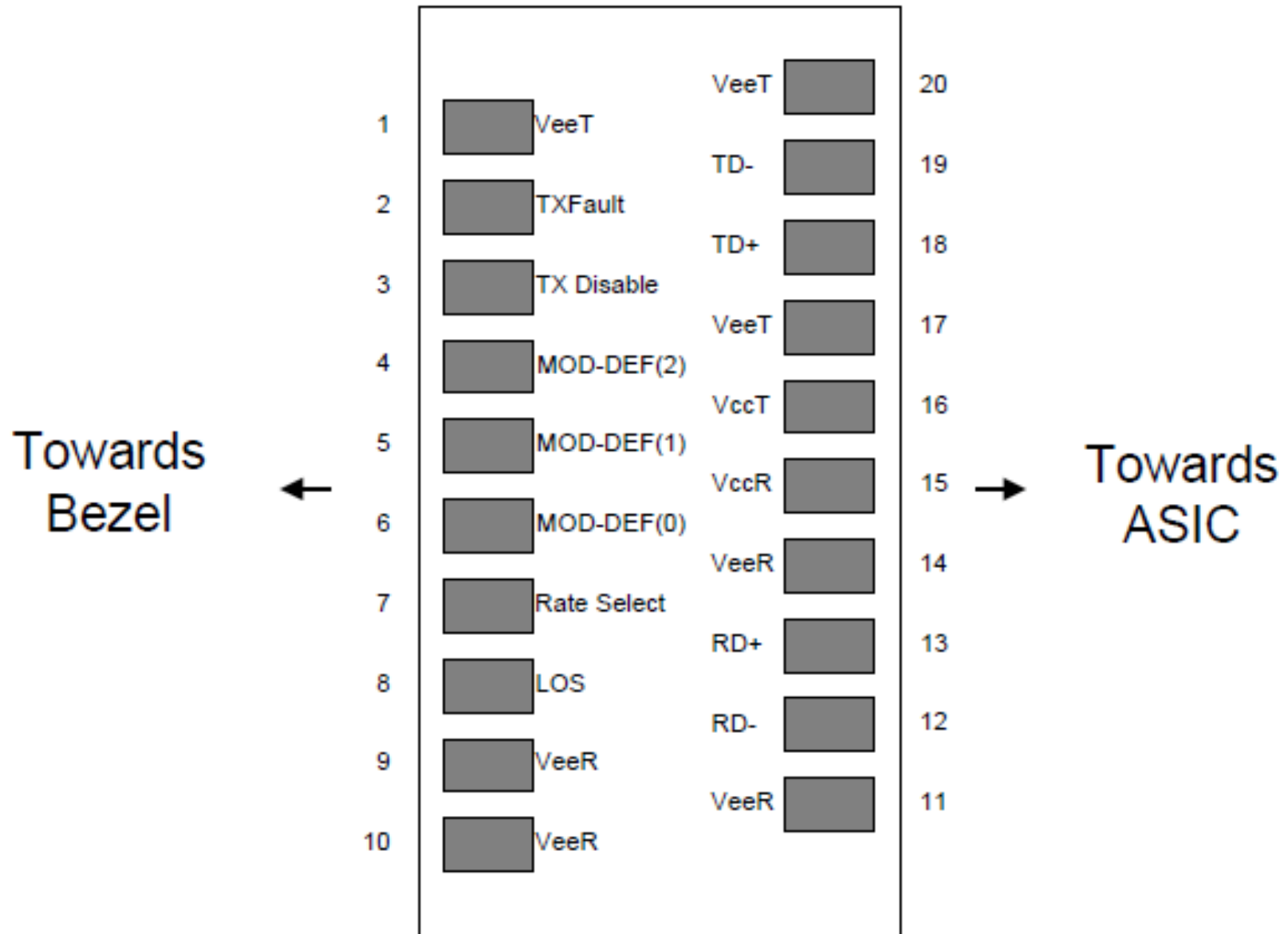 Industrial: -40°C to +85°C

SFP to Host Connector Pin Out

Diagram OF Host Board Connector Block pin numbers and names

PIN	Symbol	Name/ description	Note
1	VEET	Transmitter Ground (Common with Receiver Ground)	1
2	TFAULT	Transmitter Fault. Not supported.	
3	TDIS	Transmitter Disable. PHY disabled on high or open	2
4	MOD_DEF (2)	Module Definition 2. Data line for Serial ID.	3
5	MOD_DEF (1)	Module Definition 1. Clock line for Serial ID.	3
6	MOD_DEF (0)	Module Definition 0. Grounded within the module.	3
7	Rate Select	No connection required	
8	LOS	Loss of Signal indication. Logic 0 indicates normal operation.	3
9	VEER	Receiver Ground (Common with Transmitter Ground)	1
10	VEER	Receiver Ground (Common with Transmitter Ground)	1
11	VEER	Receiver Ground (Common with Transmitter Ground)	1
12	RD-	Receiver Inverted DATA out. AC Coupled	
13	RD+	Receiver Non-inverted DATA out. AC Coupled	
14	VEER	Receiver Ground (Common with Transmitter Ground)	1
15	VCCR	Receiver Power Supply	
16	VCCT	Transmitter Power Supply	
17	VEET	Transmitter Ground (Common with Receiver Ground	1
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled.	
19	TD-	Transmitter Inverted DATA in. AC Coupled.	
20	VEET	Transmitter Ground (Common with Receiver Ground)	1

Notes:

1.Circuit ground is connected to chassis ground

2. 2.PHY disabled on TDIS > 2.0V or open, enabled on TDIS < 0.8V

3. Should be pulled up with 4.7k - 10k Ohms on host board to a voltage between 2.0 V and 3.6 V.MOD_DEF(0) pulls line low to indicate module is plugged in.

4. LVTTL compatible with a maximum voltage of 2.5V. Not supported on XGSF-T12-02-4.

Volt Electrical Power Interface

The XGSF-T12-02-4 has an input voltage range of 3.3 V +/- 5%. The 4V maximum voltage is not allowed for continuous operation.

+3.3 Volt Electrical Power Interface						
Parameters	Symbol	Min	Typ	Max	Unit	Note/ Condition
Supply Current	Is		320	375	mA	1.2W max power over full range of voltage and temperature. See caution note below
Input Voltage	VCC	3.13	3.3	3.47	V	Referenced to GND
Maximum Voltage	Vmax			4	V	
Surge Current	Isurge			30	mA	Hot plug above steady state current. See caution note below

Caution: Power consumption and surge current are higher than the specified values in the SFP MSA

Low-Speed Signals

MOD_DEF(1) (SCL) and MOD_DEF(2) (SDA), are open drain CMOS signals (see section VII, "Serial Communication Protocol"). Both MOD_DEF(1) and MOD_DEF(2) must be pulled up to host_Vcc

Low-Speed Signals, Electronic Characteristics					
Parameters	Symbol	Min	Max	Unit	Note/Condition
SFP Output LOW	VOL	0	0.5	V	4.7k to 10k pull-up to host_Vcc, measured at host side of connector
SFP Output HIGH	VOH	host_Vcc -0.5	host_Vcc + 0.3	V	4.7k to 10k pull-up to host_Vcc, measured at host side of connector
SFP Input LOW	VIL	0	0.8	V	4.7k to 10k pull-up to Vcc, measured at SFP side of connector
SFP Input HIGH	VIH	2	Vcc + 0.3	V	4.7k to 10k pull-up to Vcc, measured at SFP side of connector

High-Speed Electrical Interface

All high-speed signals are AC-coupled internally.

High-Speed Electrical Interface, Transmission Line-SFP						
Parameters	**Symbol**	**Min**	**Type**	**Max**	**Unit**	**Note/Condition**
Line Frequency	fL		125		MHz	5-level encoding, per IEEE 802.3
Tx Output impedance	Zout, TX		100		Ohm	Differential, for all frequencies between 1MHz and 125MHz
Rx Input Impedance	Zin, RX		100		Ohm	Differential, for all frequencies between 1MHz and 125MHz

High-Speed Electrical Interface, Host-SFP						
Parameter	**Symbol**	**Min**	**Type**	**Max**	**Unit**	**Notes/Condition**
Single Ended Data Input Swing	Vinsing	250		1200	mV	Single ended
Single Ended data output swing	Voutsing	350		800	mV	Single ended
Rise/Fall Tune	Tr,Tf		175		Psec	20%-80%
Tx Input Impedance	Zin		50		Ohm	Single ended
Rx Output Impedance	Zout		50		Ohm	Single ended

General Specification

High-Speed Electrical Interface, Host-SFP						
Parameter	Symbol	Min	Type	Max	Unit	Notes/Condition
Date Rate	BR	10		1000	Mb/sec	IEEE 802.3 compatible. See Notes 2 through 4 below
Cb¿able Lenght	L			100	m	Category 5 UTP BER <10^{12}

Notes:

1.Clock tolerance is +/- 50 ppm

2.By default, the XGSF-T12-02-4 is a full duplex device in preferred master mode

3.Automatic crossover detection is enabled. External crossover cable is not required

4.10/100/1000 BASE-T operation requires the host system to have an SGMII interface with no clocks

Environmental Specifications

High-Speed Electrical Interface, Transmission Line-SFP						
Parameters	Symbol	Min	Type	Max	Unit	Note/Condition
Case Operating Temperature	Tcase	0		70	°C	XGSF-T12-02-4
		-10		80	°C	XGSF-T12-02-4E
		-40		85	°C	XGSF-T12-02-4A
Storage Temperature	Tsto	-40		85	°C	Ambient temperature

Serial Communication Protocol

XGSF-T12-02-4 support the 2-wire serial communication protocol outlined in the SFP MSA. It uses an Atmel AT24C02B 256 byte EEPROM with an address of A0h.

High-Speed Electrical Interface, Host-SFP						
Parameter	Symbol	Min	Type	Max	Unit	Notes/Condition
I 2C Clock Rate		0		100,000	Hz	

Mechanical Specifications (Unit: mm)
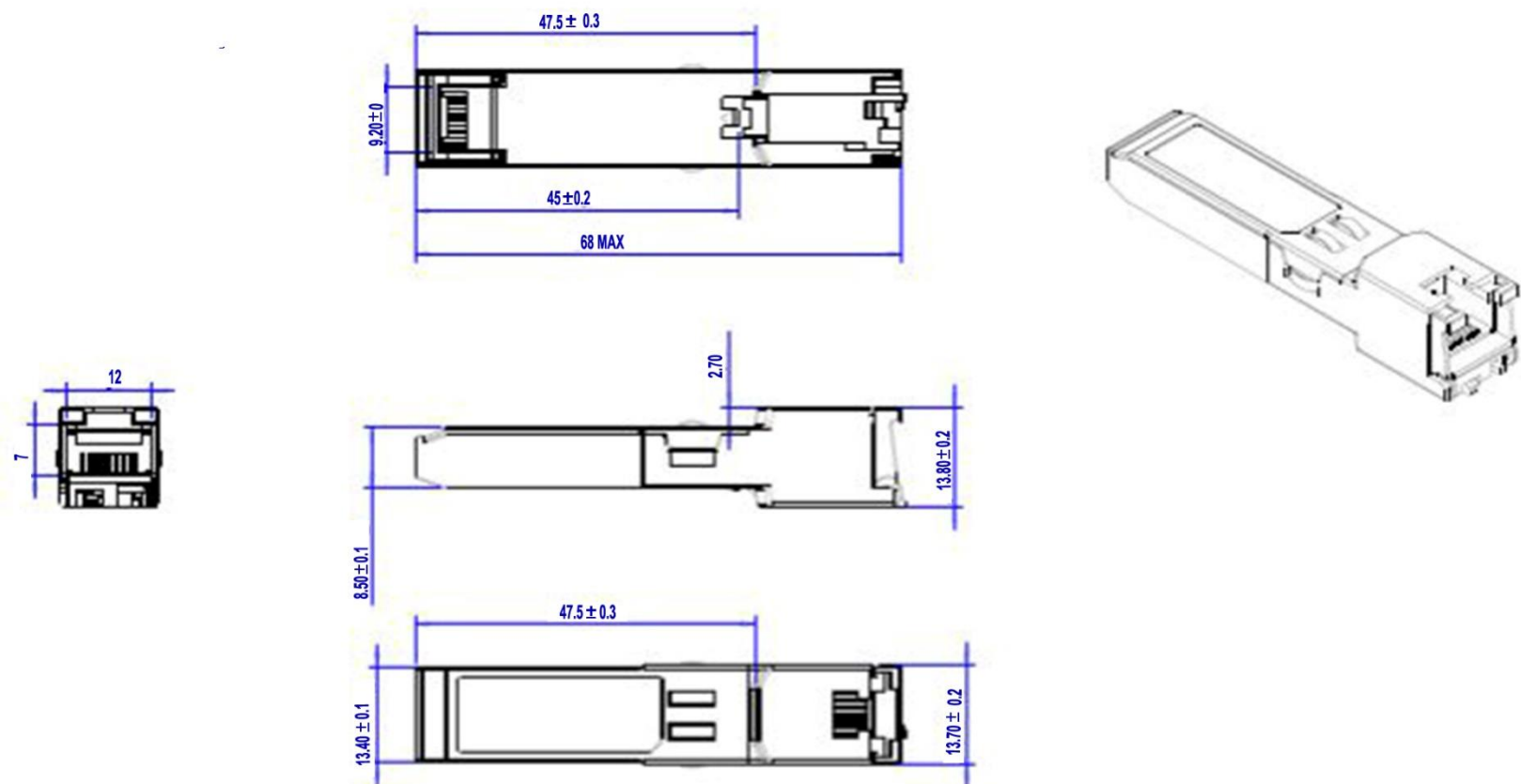
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